E6.1

UNIT E6 · CIRCUITCLASS TASK CARD

Gates in CircuitClass

UNIT PROGRESS

TASK

Test AND, OR and NOT gates with labelled switch inputs and complete their truth tables.

YOU NEED

- ☐ CircuitClass · logic mode (needs CircuitClass logic mode if not yet released)
- ☐ About 15 minutes

NEEDS CIRCUITCLASS LOGIC MODE

If logic mode is not yet available, use CircuitClass circuits mode with switches and LEDs, or a logic trainer.

1 · Build

For each gate: add two switches labelled A and B (one for NOT), the gate, and an LED labelled Z on the output.

2 · Test

A	B	Z
0	0	
0	1	
1	0	
1	1	

A	B	Z
0	0	
0	1	
1	0	
1	1	

A	Z
0	
1	

3 · Check

Do your results match the notes? Write one sentence for each gate.